

ATT7022C

V0.2

- **ATT7022B**
- **ADC** **240**
-
-

ATT7022C ATT7022B ATT7022B
 ADC
 ADC ATT7022C

ATT7022C 240
 (+ channel)
 ATT7022C ADC
 C1 gWaveAddress

0

 240 120
 3.2k ptrWaveFormRd (ptrWaveFormRd
 ATT7022C 7E)
 SPI 2byte 16bit ADC 1byte
 UA UB UC
 UA0 UB0 UC0 UA1 UB1 UC1 ... UA79 UB79 UC79
 0x00 01 00~0x00 EF 00

ATT7022C 0x1F
 0x0001F0

$$I_{startup} = INT(G * I_0 * 2^{23})$$

G 0.648

I0

INT

0.1 Ib%

0.1v

0.08%

$$I0 = 0.1 * 0.0008 = 0.00008$$

$$I_{startup} = INT(0.648 * 0.00008 * 2^{23}) = 434 = 0x0001B2$$

1 0xC0 0xC1 0x7E 0x7F

2 0x1F

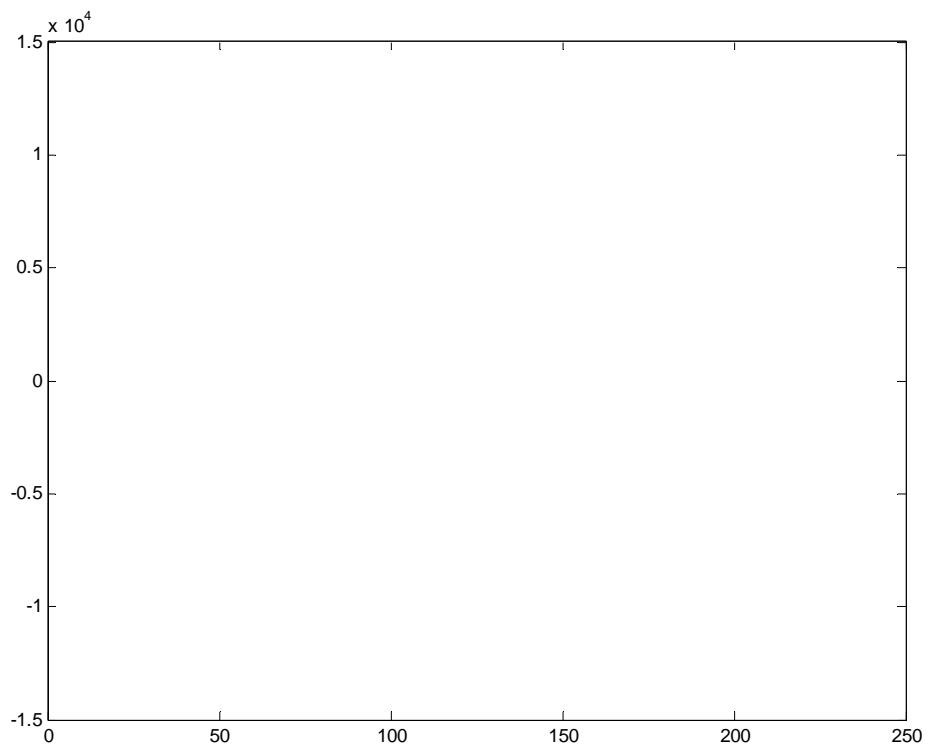
0x3E

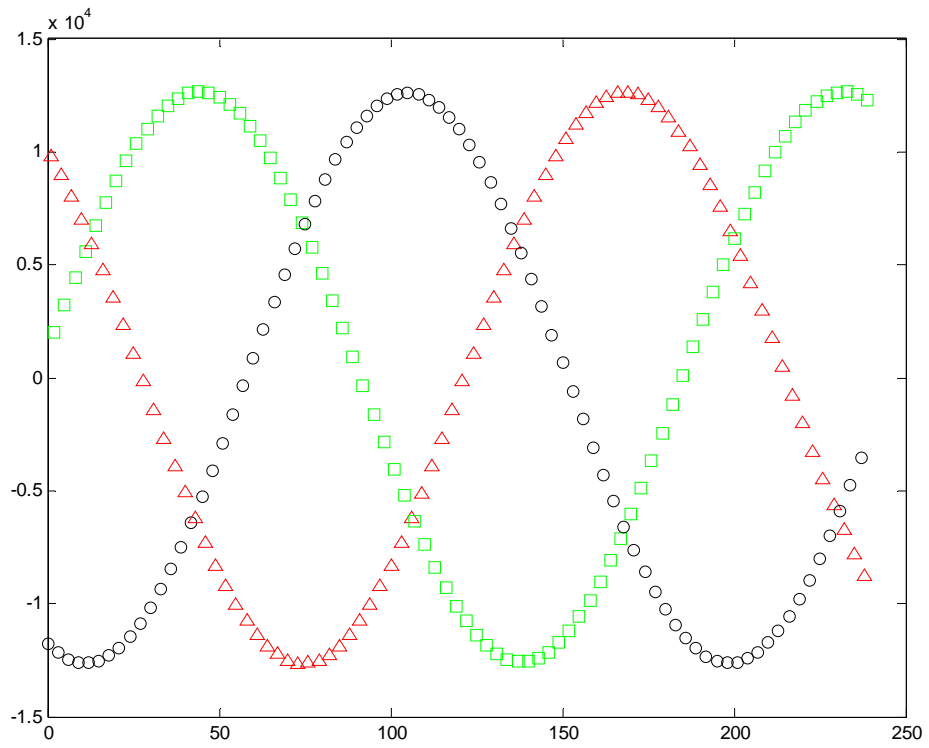
0x5F

SPI	0xC0	gWaveCommand	0x000000	<p>0xCCCCCY</p> <p>Y</p> <p>0 0x0B</p> <p>Ua\Ia\Ub\Ib\Uc\Ic\In,</p> <p>\Ua+Ia\Ub+Ib\Uc+Ic</p> <p>\Ua+Ub+Uc\Ia+Ib+Ic</p>
	0xC1	gWaveAddress	0x000000	0~239
	0x1F	W_Istartup	0x0001F0	
SPI	0x7E	ptrWaveFormRd	0x000000	<p>0~240</p> <p>240</p>
	0x7F	mWaveDatatmp	0x000000	<p>1</p> <p>gWaveAddress</p> <p>0</p>
	0x3E or 0x5F	R_checksum	<p>0x043C73</p> <p>0x16BC73</p>	

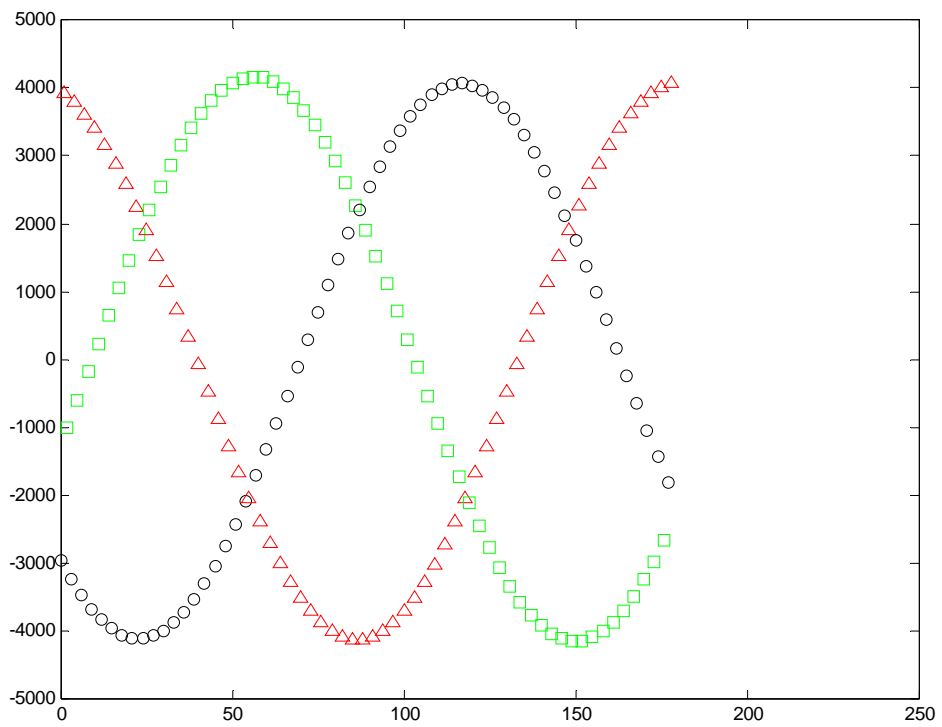
UaIa

Y=0x07 UI





IaIbIc Y=0x0B



V0.1	2008-08-14	Mxzhang	
V0.2	2008-09-17	Mxzhang	1 2